

Appl. No. 09/912,604  
Amdt. dated July 21, 2005  
Reply to Office action of April 22, 2005

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently amended) A computer system, comprising:
  - a processor;
  - a system memory coupled to said processor;
  - ~~at least one input/output device coupled to said processor;~~
  - an internal audio speaker device; and
  - an audio controller circuit that determines which of a plurality of connectors, if any, have external playback devices coupled thereto, determines whether at least one such coupled external playback device comprises a digital or an analog external playback device, and, based on such determinations, selectively transmits digital audio signals or analog audio signals to the internal audio speaker or and selectively transmits at least one of an analog audio signal, a digital audio signal, and a mute signal to external audio playback devices coupled to the computer via a plurality of output output circuitry associated with each of said connectors;
    - ~~wherein the audio controller circuit determines whether each device is analog or digital for the purpose of transmitting corresponding analog or digital audio signals to each device.~~
2. (Currently amended) The computer system of claim 1 wherein:
  - audible sounds are transmitted to and reproduced by one playback device at a time; and
  - wherein the audio controller circuit determines whether said at least one such device comprises an analog or a digital external playback device for the purpose of transmitting corresponding analog or digital audio signals~~detects the device type for that one playback device and transmit analog or digital signals as required for that device.~~

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3. (Currently amended) The computer system of claim 1 further comprising wherein said connectors comprise:

a front audio connector; and  
a rear audio connector;

wherein audio signals are transmitted by the audio controller circuit for playback by one of either the internal speaker device or a playback device coupled to the rear connector or a playback device coupled to the front audio connector.

4. (Original) The computer system of claim 3 wherein:

playback devices coupled to the rear connector have playback priority over the internal speaker device; and

playback devices coupled to the front connector have playback priority over playback devices coupled to the rear connector and the internal speaker device.

5. (Original) The computer system of claim 4 wherein the audio controller circuit determines whether to transmit analog or digital signals under the following conditions:

a) when a playback device is coupled to or removed from the front connector; and

b) when no playback device is coupled to the front connector and a playback device is coupled to or removed from the rear connector.

6. (Original) The computer system of claim 5 wherein:

if no external playback device is coupled to the computer system, the audio controller circuit transmits analog audio signals for playback by the internal speaker device.

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7. (Currently amended) An audio controller circuit that generates audible sounds for use on a computer system comprising:

a digital audio controller that generates digital audio signals reproducible by a digital audio device;

a mixed-signal codec that communicates with the digital audio controller and generates analog audio signals reproducible by an analog audio device;

a programmable logic device;

a plurality of audio output connectors, each configured to accept a mating connector coupled to an external audio device; and

a switching circuitry that transmits digital audio signals to a selected audio output connector depending on a state of a mute signal;

wherein the audio controller circuit detects whether an external audio device coupled to an output connector is an analog or digital device and transmit either analog or digital audio signals to any of the output connectors.

8. (Original) The audio controller circuit of claim 7 further comprising:

an internal analog output that transmits analog audio signals to an internal computer speaker if none of the output connectors are coupled to audio devices.

9. (Original) The audio controller circuit of claim 8 wherein the output connectors comprise:

a front output connector; and

a rear output connector;

wherein if the front output connector is coupled to an external audio device, the audio controller circuit:

asserts a first mute signal to mute the rear output connector; and

asserts a second mute signal to mute the internal analog output;

and

wherein if the front output connector is not coupled to an external audio device and the rear output connector is coupled to an external audio device, the audio controller circuit:

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asserts the second mute signal to mute the internal analog output.

10. (Original) The audio controller circuit of claim 9 wherein the switching circuit further comprises a digital output switch that:

transmits digital audio signals to the front output connector when the first mute signal is asserted; and

transmits digital audio signals to the rear output connector when the first mute signal is de-asserted.

11. (Original) The audio controller circuit of claim 9 wherein the switching circuit further comprises analog output muter that uses the first and second mute signals to selectably mute or transmit analog audio signals to the outputs such that:

if the first mute signal is asserted, the muter does not transmit analog audio signals to the rear output connector; and

if the second mute signal is asserted, the muter does not transmit analog audio signals to the internal analog output.

12. (Original) The audio controller circuit of claim 11 wherein the switching circuit further comprises a detection switch that:

transmits detection signals from the front output connector to the codec when the first mute signal is asserted; and

transmits detection signals from the rear output connector to the codec when the first mute signal is de-asserted.

13. (Original) The audio controller circuit of claim 12 wherein:

the programmable logic device transmits an interrupt that instructs the digital audio controller and codec to transmit an analog test tone to the muter when the value of either the first mute signal or the second mute signal changes.

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14. (Original) The audio controller circuit of claim 13 wherein:  
if the codec receives a detection signal, the audio controller circuit transmits analog audio signals; and  
if the codec does not receive a detection signal, the audio controller circuit transmits digital audio signals.

15. (Currently amended) A method of transmitting analog or digital audio signals to one of a plurality of computer system audio outputs comprising:  
ranking the audio outputs in terms of playback priority;  
transmitting audio signals from an audio controller circuit to the highest priority audio output to which an audio device is coupled;  
wherein:

if a playback device is removed from the audio output to which audio signals are currently being transmitted; or

if a playback device is plugged into a higher priority output;

~~determining the device type for the audio device coupled to the highest priority audio output and~~

detecting whether a transmitted analog test tone is grounded by the output device to determine whether the audio device coupled to the highest priority audio output comprises a digital or an analog device; and

transmitting the appropriate analog or digital audio signals to that output.

16. (Currently amended) The method of claim 15 further comprising:  
generating an interrupt to indicate to the computer system that a determination is to be made as to whether the device comprise a digital or an analog device~~device type must be determined.~~

17. (Currently amended) The method of claim 16 wherein the process of determining the device type further comprises:

transmitting an analog test-tone to the highest priority output device; and

~~detecting whether the analog test tone is grounded by the output device;~~

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~~wherein if the test tone is grounded, the device is a digital device and wherein if the test tone is detected, the device is an analog device.~~

18. (Currently amended) The method of claim ~~47~~15 wherein the test tone is transmitted to the right channel of a stereo output device.

19. (Currently amended) The method of claim ~~47~~15 wherein the audio signals and test tone are directed to and from the highest priority output device using switches.

20. (Original) The method of claim 16 wherein the interrupt is generated by a programmable logic device and the duration of the interrupt is 500 microseconds.